

Pilon De Poulet Ricardo Mijoteuse

M Tight

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sinyal digital yang ada pada inputnya

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memiliki lebih dari satu input a b c dll

tetapi umumnya hanya memiliki satu

output digital q

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input bilangan biner dengan teori

matematika boolean seperti yang kita

ketahui bilangan biner sendiri terdiri

dari angka 1 dan 0 logic gate ini

direpresentasikan menggunakan tabel

kebenaran jika memiliki nilai benar

true akan ditunjukkan dengan angka 1

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memiliki satu keluaran dan dua

masukan beberapa gerbang logika

seperti gerbang not atau inverter hanya

memiliki satu input dan satu output input dari gerbang logika dirancang untuk hanya menerima data biner hanya rendah 0 atau tinggi

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