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web atm software the atm automated teller machine design

is a combination of hardware and software the hardware of the machine is mainly designed for deposits of cash withdrawal payments of credit card for reporting the information of account the software of atm is designed to control atm transactions channels on centralized databases
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web the developed design will be modeled using verilog hdl language which is a hardware description language hdl used to describe a digital system the verification of developed model will be made by identifying the suitable test cases in a test bench

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web 2 automatic teller machine using verilog 2022 02 22 systems design and while vliw is a running theme throughout the book embedded computing is the core topic embedded computing examines both in a book filled with fact and opinion based on the authors many years of r d experience features complemented by a unique professional quality